

**Notice of Allowability**

Application No.

09/892,647

Examiner

Prabodh M. Dharja

Applicant(s)

LIM, JOO SOO

Art Unit

2629

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 08-22-2006.
2. ☒ The allowed claim(s) is/are 1-15.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All    b) ☐ Some\*    c) ☐ None    of the:
    1. ☒ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

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1. **Status:** Please all the replies and correspondence should be addressed to Examiner's new art unit 2629. Receipt is acknowledged of papers submitted on 08-22-2006 under amendments, which have been placed of record in the file. Claims 1-15 are pending in this action.

***Response to Amendment***

2. Applicant has amended independent claims 1 and 12 as per allowed limitations. Claims 8-11 and 15 were allowed. Applicant has amended claims 1 and 12 by adding limitations of allowed claims 8 and 15. Applicant's arguments, see Remark, filed 08-22-06, with respect to Claims 1,12 regarding "A method of testing a liquid crystal display panel, having a plurality of data lines and a plurality of gate lines crossing each other and a plurality of liquid crystal pixel cells arranged where the gate lines cross the data lines, the method comprising: applying data voltages to the data lines; applying a mode setting signal to a gate driver connected with the gate lines; sequentially scanning the gate lines in a direction identified by the mode setting signal to display a test pattern on the display panel at an increased brightness; and identifying any defective pixel cells among the plurality of liquid crystal pixel cells from the test pattern" have been fully considered, searched and are persuasive as they do overcome prior art rejection; and also telephone interview with applicant's representative, applicant has agreed to do examiner amendments to claim 12, which puts application number 09892647 in condition for allowance.

3. Applicant amended the independent claims 1 and 12 has no new matters added to the claims. The amended claims do have support in specification.

### **EXAMINER'S AMENDMENT**

4. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a personal telephone interview with applicant's representative George G. Ballas on September 18, 2006.

The application has been amended as follows:

#### **In the Claims**

Claim 12, page 5, Line 6, after word "defect" delete word "defection" and add word "detection".

#### ***Allowable Subject Matter***

5. Claims 1-15 are allowed.

6. The following is an examiner's statement of reasons for allowance:

Applicant's argument filed on 08-22-2006 are convincing. As argued by applicant in remarks under claim rejection page 7, paragraph 2, the prior art of Tomita (6,275,061 B1) in

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view of Knodoh et al. (2003/0034944 A1) and Toyoda et al. (6,448,952 B1) fails to recite or disclose the uniquely distinct features represented by underlined bold claim below;

A method of testing a liquid crystal display panel, having a plurality of data lines and a plurality of gate lines crossing each other and a plurality of liquid crystal pixel cells arranged where the gate lines cross the data lines, the method comprising: applying data voltages to the data lines; **applying a mode setting signal to a gate driver connected with the gate lines; sequentially scanning the gate lines in a direction identified by the mode setting signal to display a test pattern on the display panel at an increased brightness; and identifying any defective pixel cells among the plurality of liquid crystal pixel cells from the test pattern.**

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance.

### ***Conclusion***

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Prabodh M Dharia whose telephone number is 571-272-7668. The examiner can normally be reached on M-F 8AM to 5PM.

8. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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9. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

PD

AU2629

September 17, 2006



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